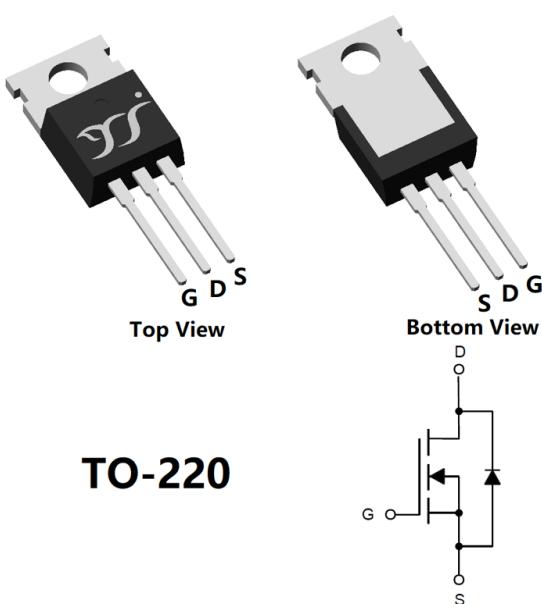


N-Channel Enhancement Mode Field Effect Transistor



Product Summary

- V_{DS} 100V
- I_D 70A
- $R_{DS(on)}$ (at $V_{GS}=10V$) <8.6 mohm
- $R_{DS(on)}$ (at $V_{GS}=6V$) <13 mohm
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power switching application
- Hard switched and high frequency circuits
- UPS

■ Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	100	V
Gate-source Voltage		V_{GS}	± 20	V
Drain Current ^A (Package Limited)	$T_c=25^\circ C$	I_D	70	A
	$T_c=100^\circ C$		44	
Pulsed Drain Current ^B		I_{DM}	280	A
Avalanche energy ^C		EAS	200	mJ
Total Power Dissipation ^D	$T_c=25^\circ C$	P_D	125	W
	$T_c=100^\circ C$		50	
Junction and Storage Temperature Range		T_J, T_{STG}	-55~+150	°C

■ Thermal resistance

		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^E	$t \leq 10S$	$R_{\theta JA}$	12	15	°C/W
Thermal Resistance Junction-to-Ambient ^E	Steady-State		50	60	
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	0.8	1.0	

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJP70G10B	B1	YJP70G10B	50	/	5000	Tube



YJP70G10B

■ Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=100, V_{\text{GS}}=0\text{V}$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}= \pm 20\text{V}, V_{\text{DS}}=0\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2	2.8	4	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}= 10\text{V}, I_{\text{D}}=20\text{A}$		7.2	8.6	$\text{m}\Omega$
		$V_{\text{GS}}= 6\text{V}, I_{\text{D}}=20\text{A}$		10	13	$\text{m}\Omega$
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=20\text{A}, V_{\text{GS}}=0\text{V}$			1.3	V
Maximum Body-Diode Continuous Current	I_{S}				70	A
Gate resistance	R_{G}	f= 1 MHz, Open drain		0.68		Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		2270		pF
Output Capacitance	C_{oss}			797		
Reverse Transfer Capacitance	C_{rss}			36		
Switching Parameters						
Total Gate Charge	Q_{g}	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=50\text{V}, I_{\text{D}}=25\text{A}$		32		nC
Gate-Source Charge	Q_{gs}			11.1		
Gate-Drain Charge	Q_{gd}			4.78		
Reverse Recovery Charge	Q_{rr}	$I_{\text{F}}=20\text{A}, \text{di}/\text{dt}=100\text{A}/\text{us}$		84		ns
Reverse Recovery Time	t_{rr}			51.5		
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=10\text{V}, V_{\text{DD}}=50\text{V}, I_{\text{D}}=25\text{A}$ $R_{\text{GEN}}=2.2\Omega$		51		ns
Turn-on Rise Time	t_{r}			14.4		
Turn-off Delay Time	$t_{\text{D(off)}}$			69.2		
Turn-off fall Time	t_{f}			20.6		

- A. The maximum current rating is package limited.
- B. Repetitive rating; pulse width limited by max. junction temperature.
- C. $V_{\text{DD}}=50\text{V}$, $R_{\text{G}}=25\Omega$, $L=0.5\text{mH}$, starting $T_J=25^\circ\text{C}$.
- D. P_{d} is based on max. junction temperature, using junction-case thermal resistance.
- E. The value of $R_{\theta,\text{JA}}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The Power dissipation PDSM is based on $R_{\theta,\text{JA}} \leq 10\text{s}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.



■ Typical Performance Characteristics

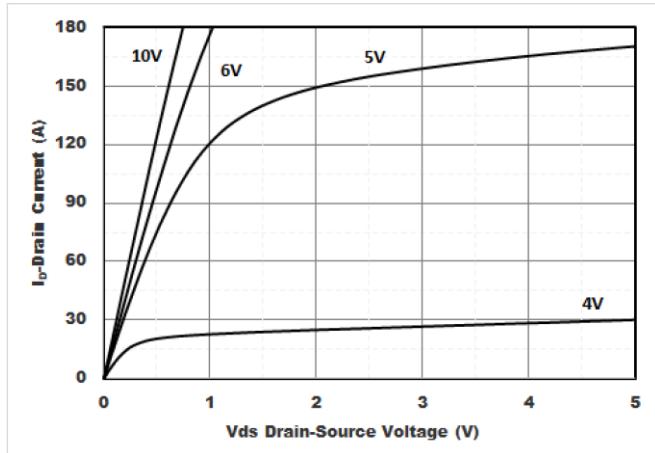


Figure1. Output Characteristics

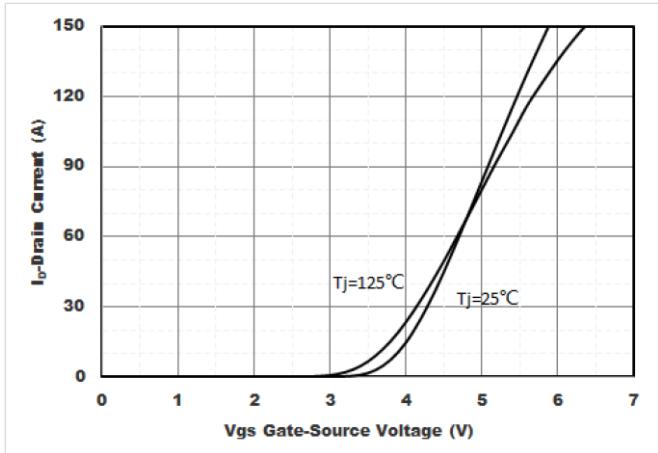


Figure2. Transfer Characteristics

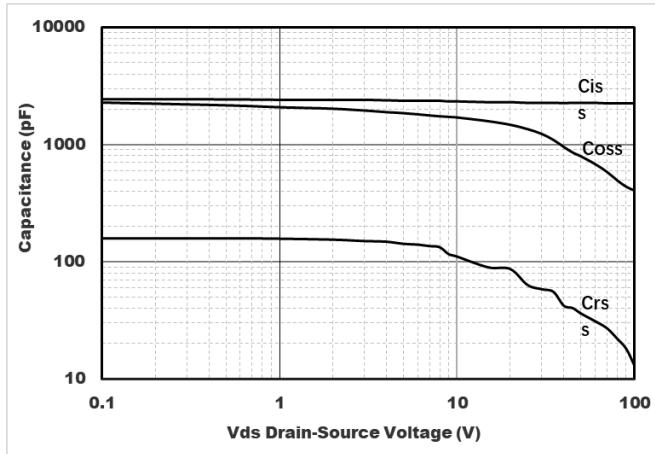


Figure3. Capacitance Characteristics

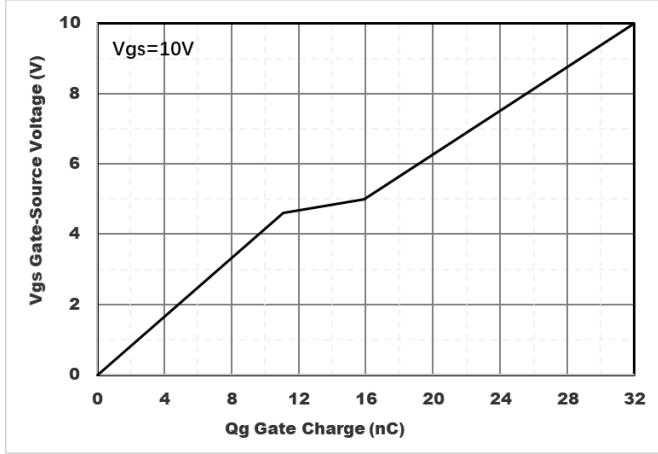


Figure4. Gate Charge

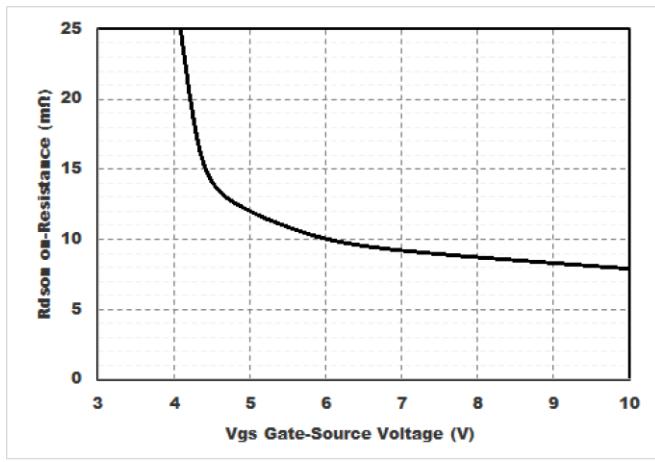


Figure5. : On-Resistance vs. Drain Current and Gate Voltage

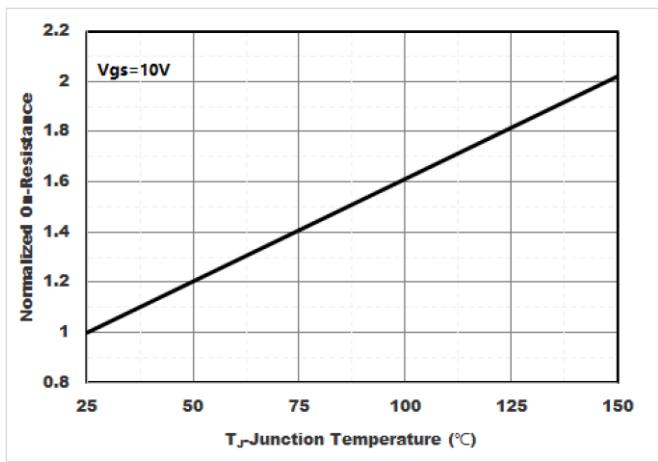


Figure6. Normalized On-Resistance

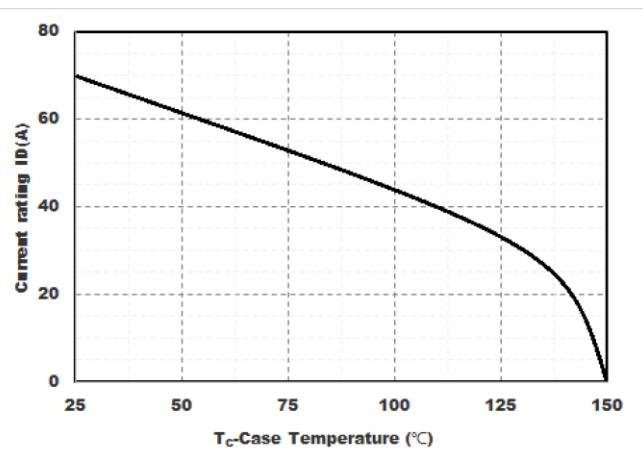


Figure7. Drain current

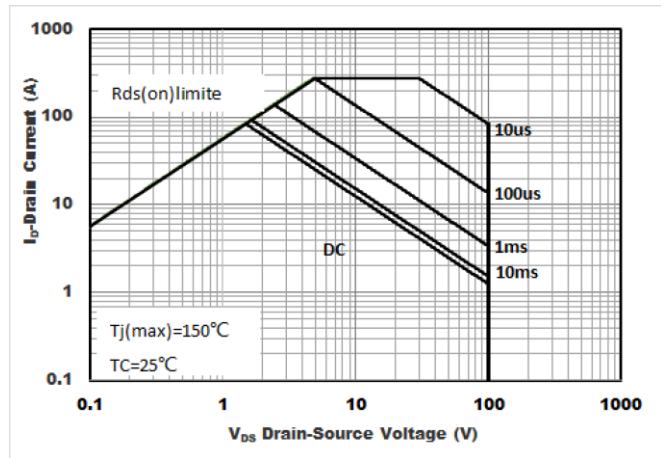


Figure8.Safe Operation Area

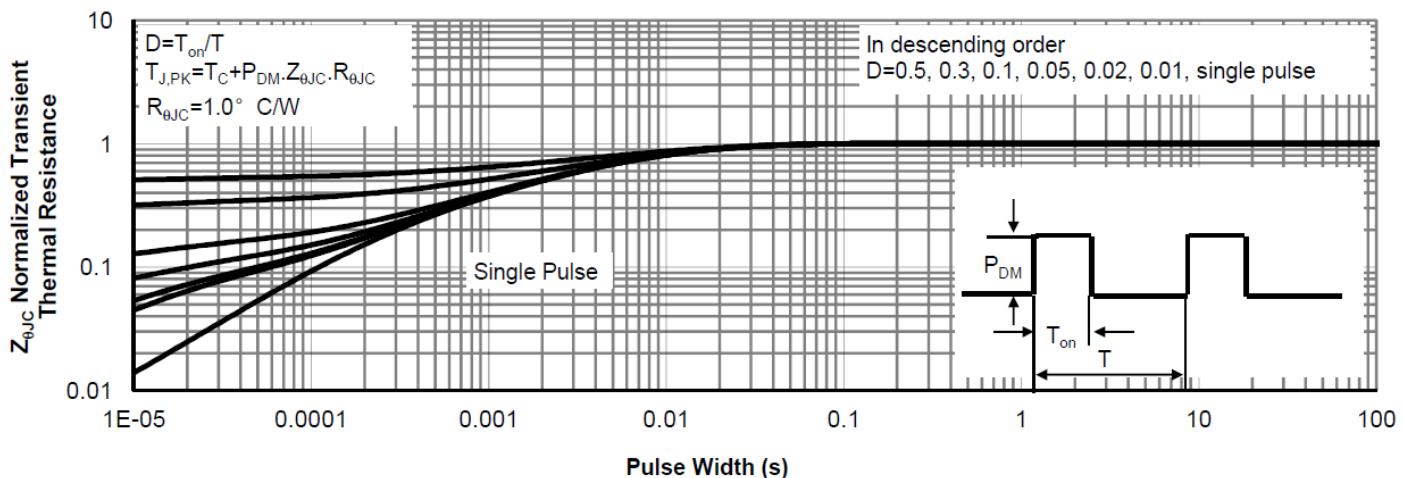
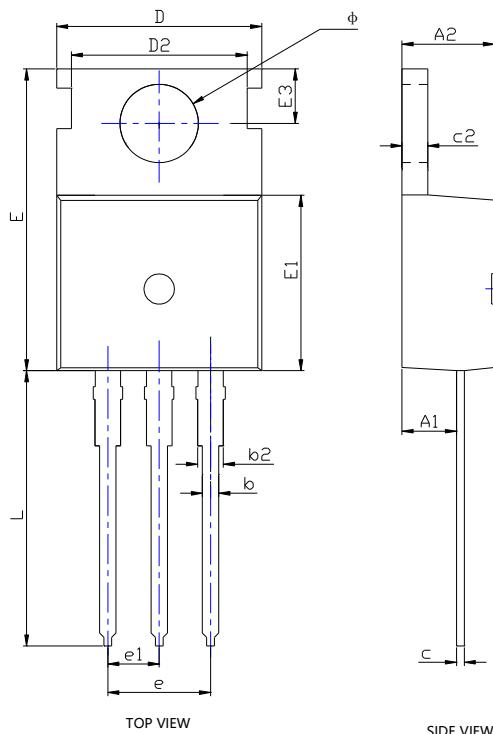


Figure9.Normalized Maximum Transient thermal impedance



■ TO-220AB-C Package information



SYMBOL	DIMENSIONS		Millimeter	
	INCHES		MIN.	MAX.
A1	0.091	0.098	2.300	2.500
A2	0.173	0.181	4.400	4.600
b	0.028	0.035	0.700	0.900
b2	0.049	0.056	1.250	1.420
c	0.018	0.022	0.450	0.550
c2	0.049	0.053	1.250	1.350
D	0.382	0.402	9.700	10.200
D1	0.295	0.331	7.500	8.400
D2	0.335	0.350	8.500	8.900
E	0.602	0.634	15.300	16.100
E1	0.358	0.366	9.100	9.300
E2	0.497	0.525	12.630	13.330
E3	0.108BSC		2.750BSC	
e	0.200BSC		5.080BSC	
e1	0.100BSC		2.540BSC	
L	0.512	0.531	13.000	13.500
L1	---	0.138	---	3.500
Ø	0.140	0.148	3.550	3.750

NOTE:
1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE
BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.



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