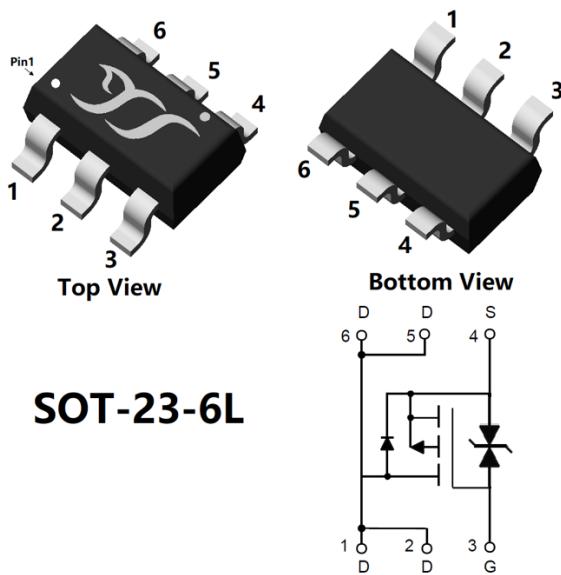


## P-Channel Enhancement Mode Field Effect Transistor

**SOT-23-6L**

### Product Summary

- $V_{DS}$  - 20V
- $I_D$  - 5A
- $R_{DS(on)}$  (at  $V_{GS}=-4.5V$ )  $<45m\Omega$
- ESD Protected Up to 2.0kV (HBM)

### General Description

- Low  $R_{DS(on)}$  & FOM
- Extremely low switching loss
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Part no. with suffix "Q" means AEC-Q101 qualified
- Halogen Free

### Applications

- Power management
- Portable equipment

#### ■ Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		$V_{DS}$	-20	V
Gate-source Voltage		$V_{GS}$	$\pm 10$	V
Drain Current	$T_A=25^\circ C$	$I_D$	-5	A
	$T_A=100^\circ C$		-3.2	
Pulsed Drain Current <sup>A</sup>		$I_{DM}$	-30	A
Total Power Dissipation <sup>C</sup>	$T_A=25^\circ C$	$P_D$	1	W
	$T_A=100^\circ C$		0.4	
Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~+150	°C

#### ■ Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient <sup>D</sup>	Steady-State	$R_{\theta JA}$	100	120	°C/W

#### ■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJJ3415KCQ	F2	3415A	3000	30000	120000	7" reel



# YJJ3415KCQ

## ■ Electrical Characteristics ( $T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-20	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 10\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 10$	$\mu\text{A}$
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.5	-0.85	-1.25	V
Static Drain-Source On-Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-5\text{A}$	-	28	45	$\text{m}\Omega$
Diode Forward Voltage	$V_{\text{SD}}$	$I_{\text{S}}=-5\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.9	-1.2	V
Gate resistance	$R_{\text{G}}$	f=1MHz	-	17	-	$\Omega$
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	540	-	pF
Output Capacitance	$C_{\text{oss}}$		-	120	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	100	-	
<b>Switching Parameters</b>						
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$	-	13	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	2	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	2	-	
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_{\text{F}}=-5\text{A}, \text{di/dt}=100\text{A/us}$	-	2.5	-	nC
Reverse Recovery Time	$t_{\text{rr}}$		-	20	-	ns
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-10\text{V}, I_{\text{D}}=-5\text{A}$ $R_{\text{GEN}}=2.2\Omega$	-	5	-	ns
Turn-on Rise Time	$t_{\text{r}}$		-	47	-	
Turn-off Delay Time	$t_{\text{D(off)}}$		-	52	-	
Turn-off fall Time	$t_{\text{f}}$		-	69	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B.  $P_{\text{d}}$  is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.

C. The value of  $R_{\theta_{\text{JA}}}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper, in the still air environment with  $T_A=25^\circ\text{C}$ .

The maximum allowed junction temperature of  $150^\circ\text{C}$ . The value in any given application depends on the user's specific board design.

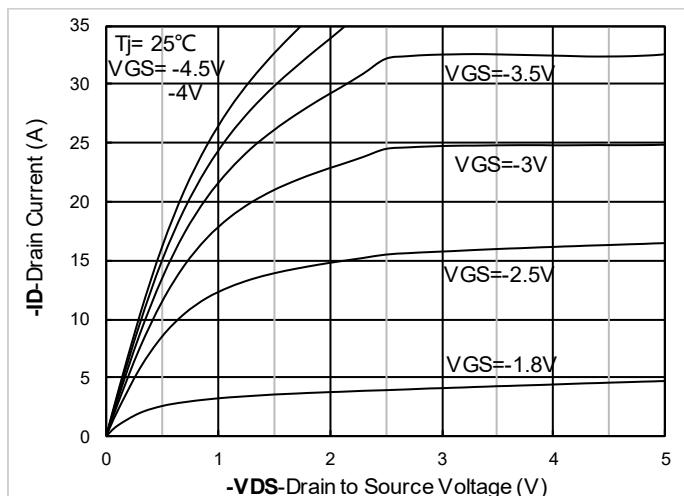
**■Typical Electrical and Thermal Characteristics Diagrams**

Figure 1. Output Characteristics

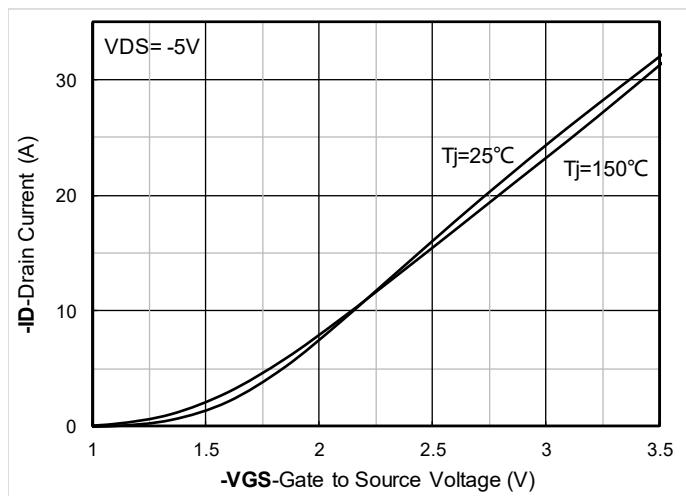


Figure 2. Transfer Characteristics

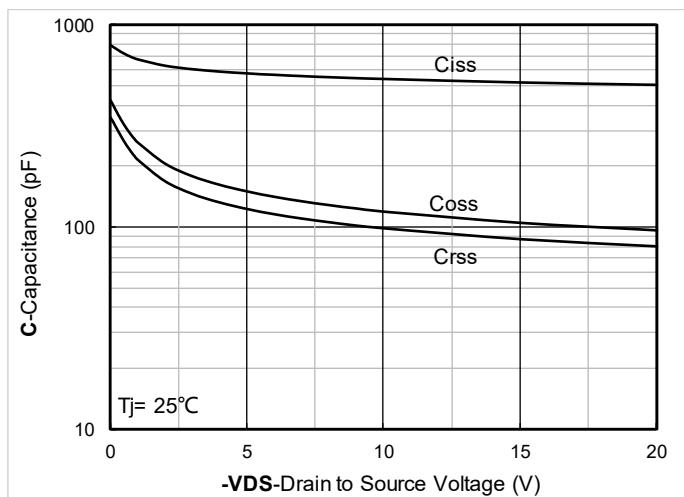


Figure 3. Capacitance Characteristics

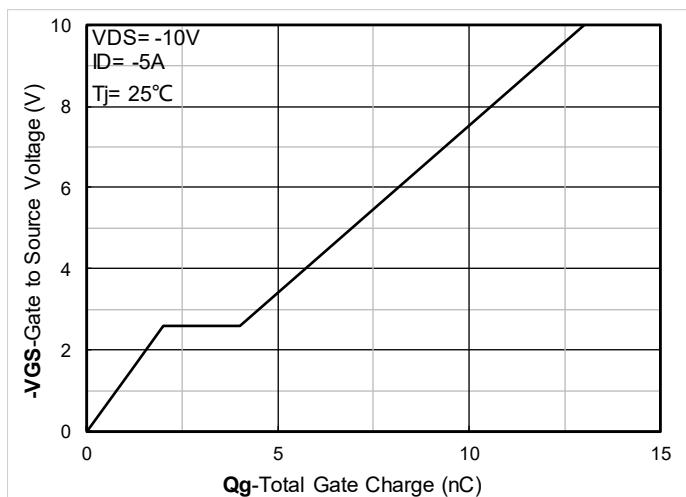


Figure 4. Gate Charge

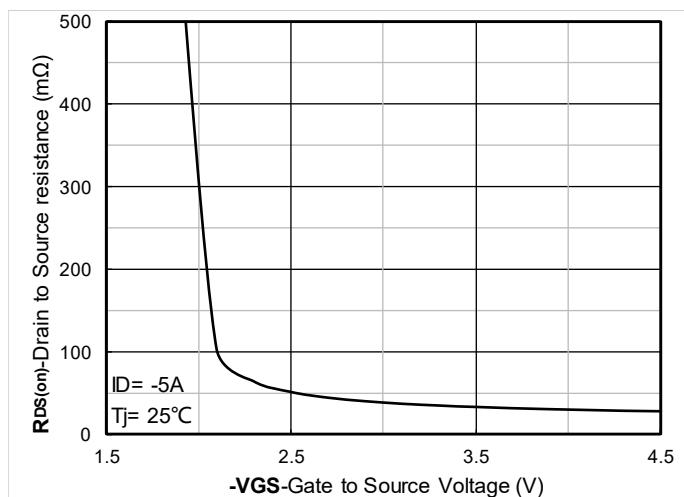


Figure 5. On-Resistance vs Gate to Source Voltage

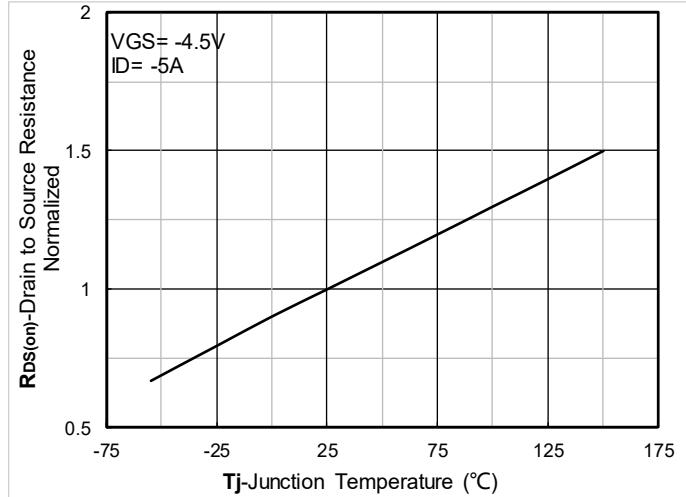


Figure 6. Normalized On-Resistance

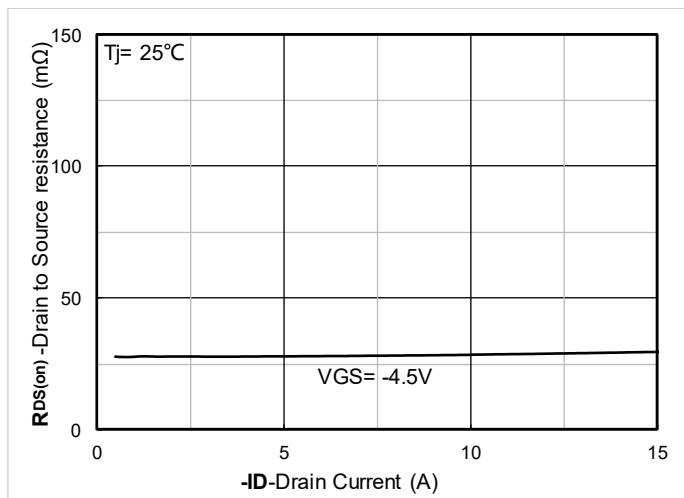


Figure 7.  $R_{DS(\text{on})}$  VS Drain Current

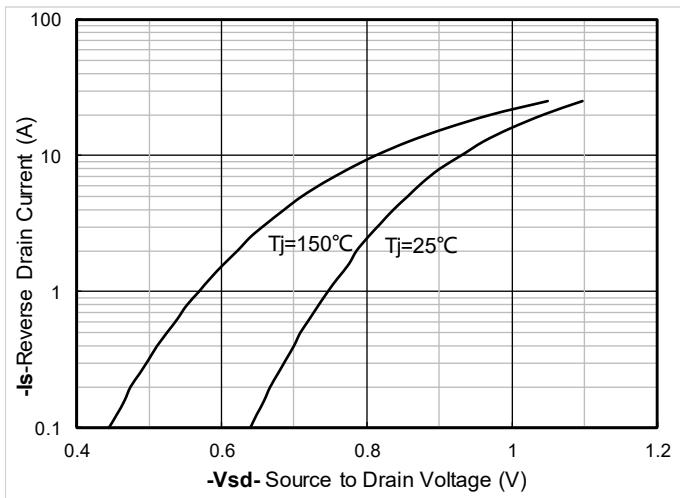


Figure 8. Forward characteristics of reverse diode

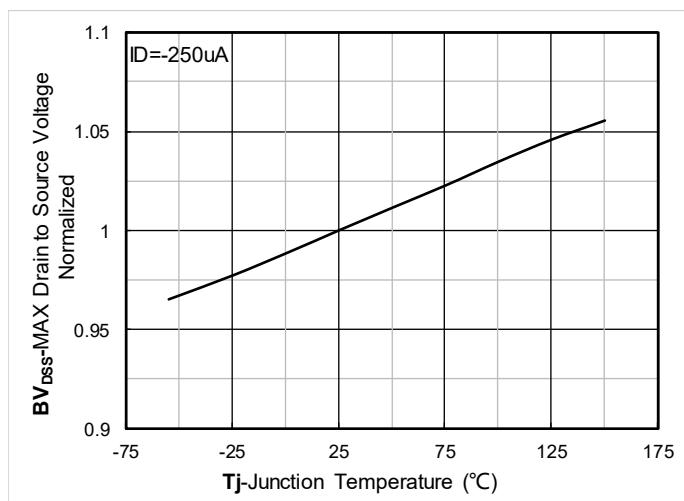


Figure 9. Normalized breakdown voltage

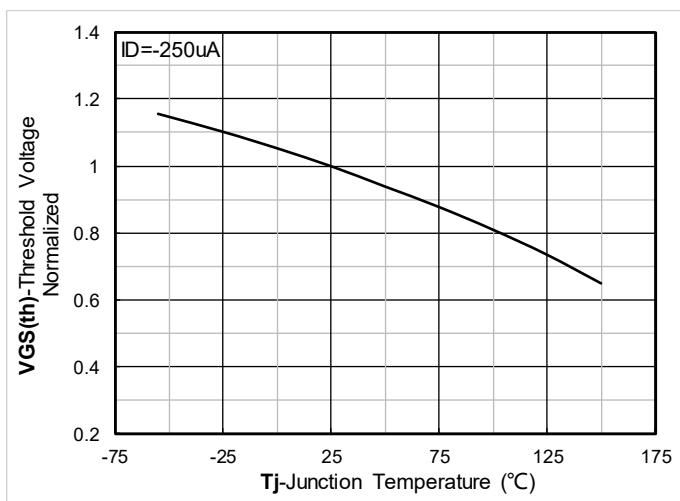


Figure 10. Normalized Threshold voltage

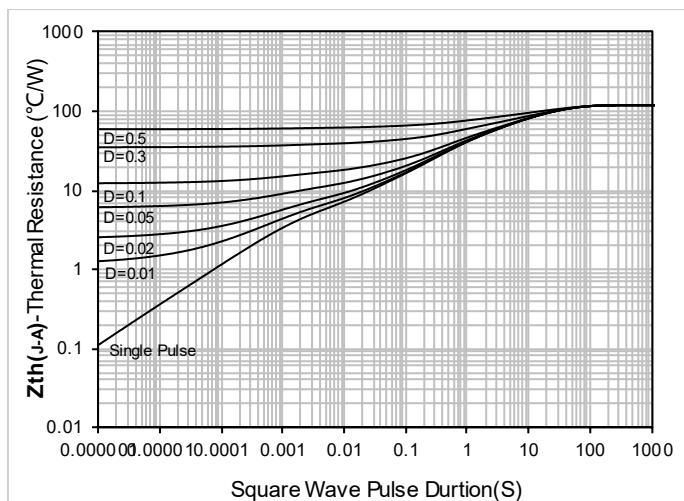


Figure 11. Maximum Transient Thermal Impedance

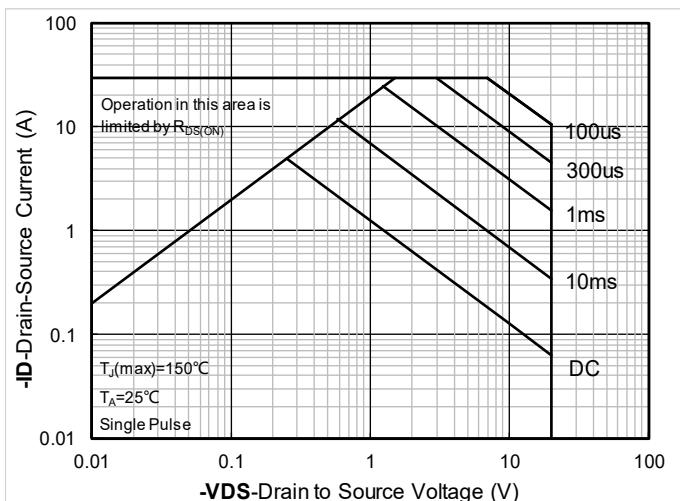
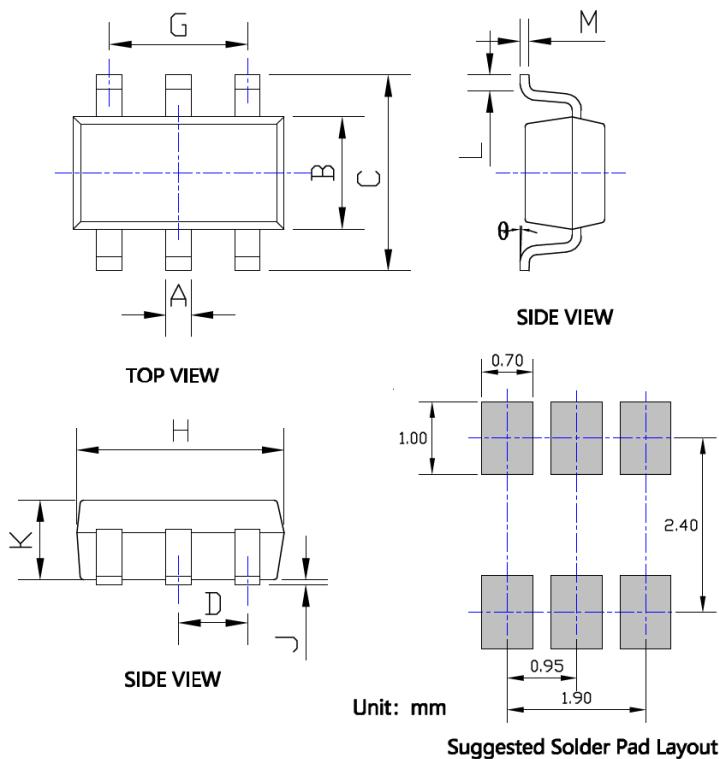


Figure 12. Safe Operation Area

**■ SOT-23-6L Package information**

SYMBOL	DIMENSIONS			
	INCHES	Millimeter	MIN.	MAX.
A	0.012	0.020	0.300	0.500
B	0.059	0.067	1.500	1.700
C	0.104	0.116	2.650	2.950
D	0.037BSC	0.950BSC		
G	0.075BSC	1.900BSC		
H	0.111	0.119	2.820	3.020
J	0.000	0.004	0.000	0.100
K	0.041	0.045	1.050	1.150
L	0.012	0.024	0.300	0.600
M	0.004	0.008	0.100	0.200
θ	0°	8°	0°	8°

**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.



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