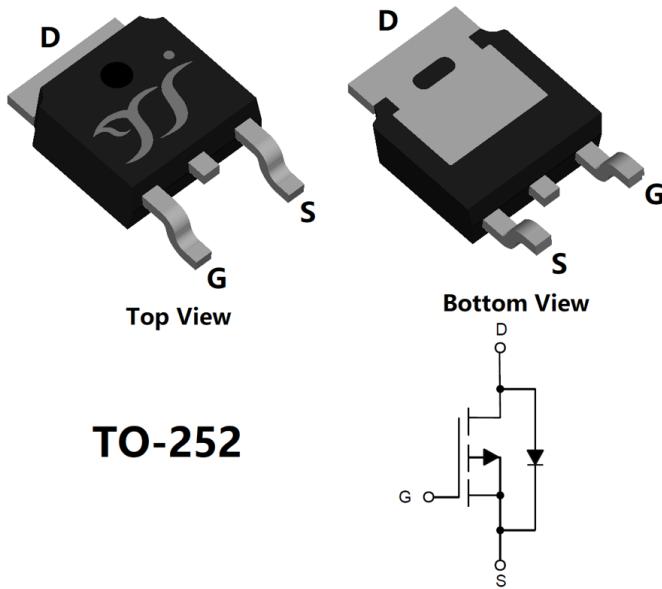


P-Channel Enhancement Mode Field Effect Transistor



Product Summary

- V_{DS} -40 V
- I_D -50 A
- $R_{DS(on)}$ (at $V_{GS}=-10V$) $<15\text{ m}\Omega$
- $R_{DS(on)}$ (at $V_{GS}=-4.5V$) $<22\text{ m}\Omega$
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power management
- Portable equipment

■ Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage	$T_J \geq 25^\circ\text{C}; T_J \leq 150^\circ\text{C}$		V_{DS}	-	-40	V
Gate-source Voltage	$T_J \leq 150^\circ\text{C}; \text{DC}$		V_{GS}	-20	20	
Continuous Drain Current	Steady-State	$T_A=25^\circ\text{C}, V_{GS}= 10\text{V}$	I_D	-	-8	A
		$T_A=100^\circ\text{C}, V_{GS}= 10\text{V}$		-	-7	
Continuous Drain Current	Steady-State	$T_C=25^\circ\text{C}, V_{GS}= 10\text{V}$		-	-50	
		$T_C = 100^\circ\text{C}, V_{GS}= 10\text{V}$		-	-31	
Pulsed Drain Current ^A	$T_C=25^\circ\text{C}, t_p \leq 10\mu\text{s}$		I_{DM}	-	-200	
Maximum Body-Diode Continuous Current			I_S		-50	
Avalanche energy ^B (non-repetitive)	$V_G=-10\text{V}, R_G=25\Omega, L=0.5\text{mH}, IAS=-9.5\text{A}$		EAS	-	22.5	mJ
Total Power Dissipation ^C	Steady-State	$T_A=25^\circ\text{C}$	P_D	-	2.5	W
		$T_A=100^\circ\text{C}$		-	1	
Total Power Dissipation ^C	Steady-State	$T_C=25^\circ\text{C}$		-	83	
		$T_C = 100^\circ\text{C}$		-	33	
Junction and Storage Temperature Range			T_J, T_{STG}	-55	150	°C

■ Thermal resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^D	$R_{\theta JA}$	40	50	°C/W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	1.2	1.5	

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJD50P04AJ	F1	YJD50P04AJ	2500	/	25000	13" reel



YJD50P04AJ

■ Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-40\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	±100	nA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-2.5	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-20\text{A}$		11.5	15	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-20\text{A}$	-	14	22	
Diode Forward Voltage	V_{SD}	$I_{\text{s}}=-20\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.85	-1.2	V
Gate resistance	R_{G}	$f=1\text{MHz}$	-	10	-	Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	3500	-	pF
Output Capacitance	C_{oss}		-	270	-	
Reverse Transfer Capacitance	C_{rss}		-	230	-	
Switching Parameters						
Total Gate Charge	Q_{g}	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-20\text{V}, I_{\text{D}}=-25\text{A}$	-	73.3	-	nC
Gate-Source Charge	Q_{gs}		-	8.9	-	
Gate-Drain Charge	Q_{gd}		-	15.3	-	
Reverse Recovery Charge	Q_{rr}	$I_{\text{F}}=-25\text{A}, \text{di}/\text{dt}=100\text{A}/\mu\text{s}$	-	12.9	-	nC
Reverse Recovery Time	t_{rr}		-	25.4	-	ns
Turn-on Delay Time	$t_{\text{D}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-20\text{V}, I_{\text{D}}=-25\text{A}$ $R_{\text{GEN}}=6\Omega$	-	13.6	-	ns
Turn-on Rise Time	t_{r}		-	11.8	-	
Turn-off Delay Time	$t_{\text{D}(\text{off})}$		-	201.5	-	
Turn-off fall Time	t_{f}		-	92.5	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B. $T_J=25^\circ\text{C}, V_{\text{DD}}=-70\text{V}, V_{\text{G}}=-10\text{V}, L=0.5\text{mH}, I_{\text{AS}}=-9.5\text{A}$.

C. P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.

D. The value of $R_{\theta JA}$ is measured with the device mounted on the minimum recommend pad size, in the still air environment with $TA = 25^\circ\text{C}$. The maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.

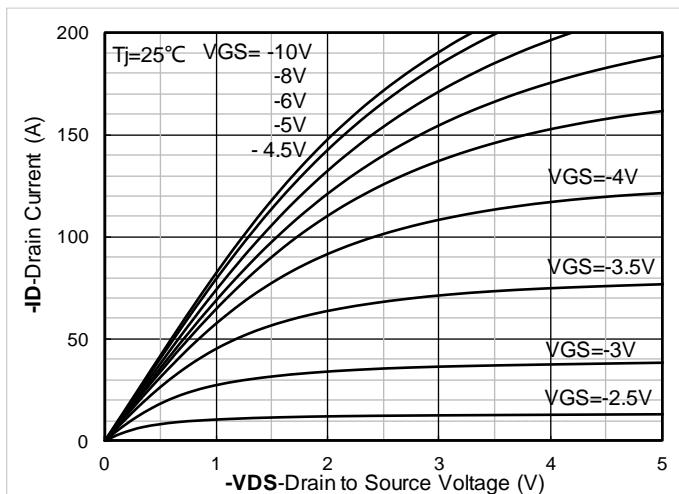
**■Typical Electrical and Thermal Characteristics Diagrams**

Figure 1. Output Characteristics

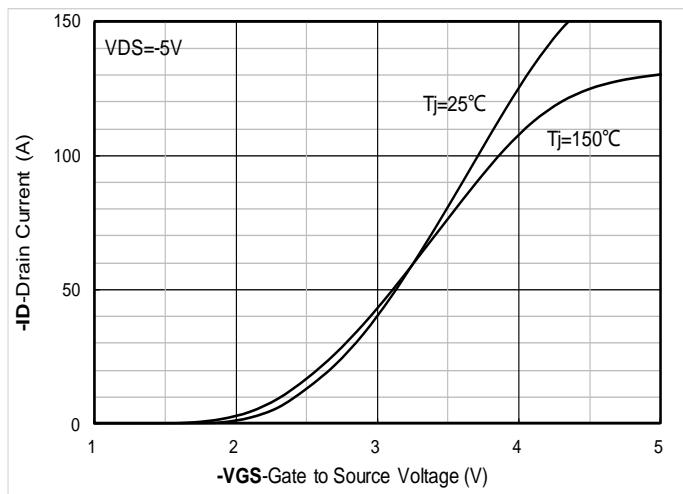


Figure 2. Transfer Characteristics

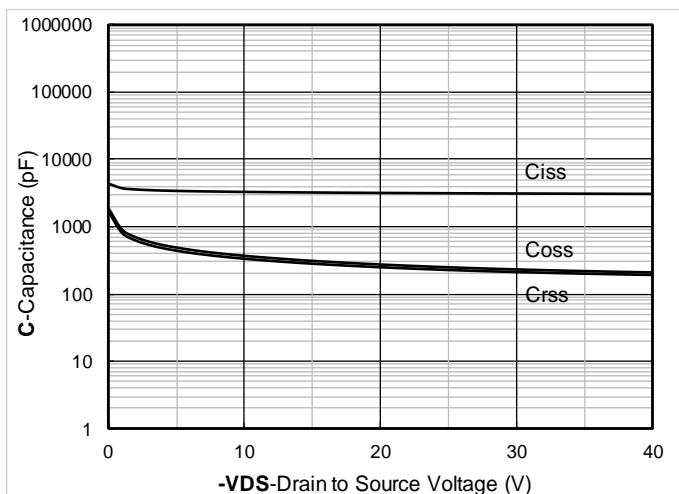


Figure 3. Capacitance Characteristics

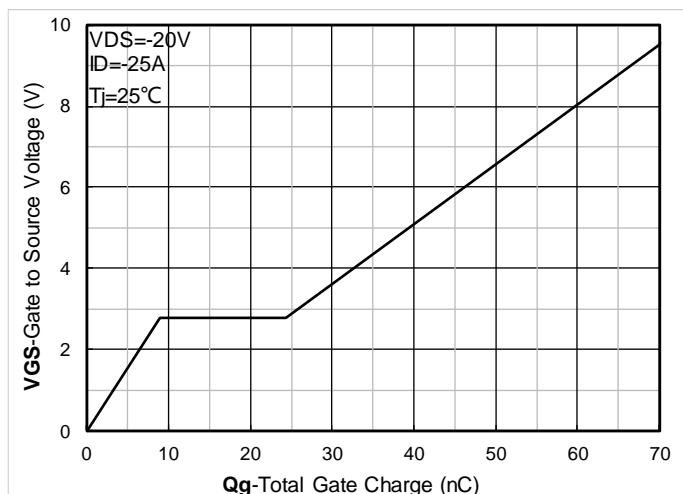


Figure 4. Gate Charge

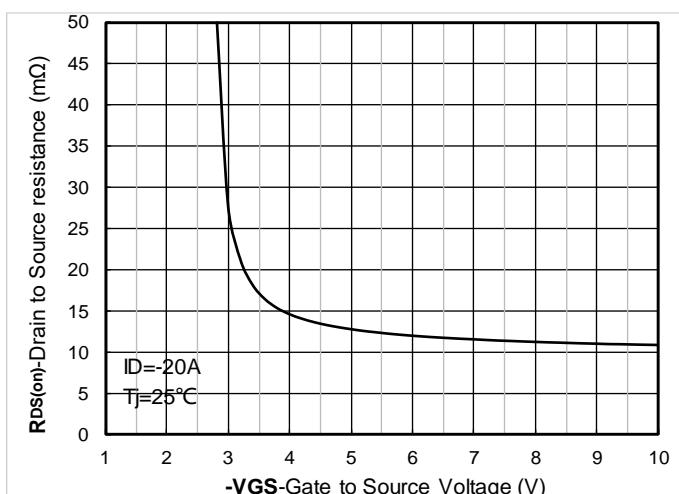


Figure 5. On-Resistance vs Gate to Source Voltage

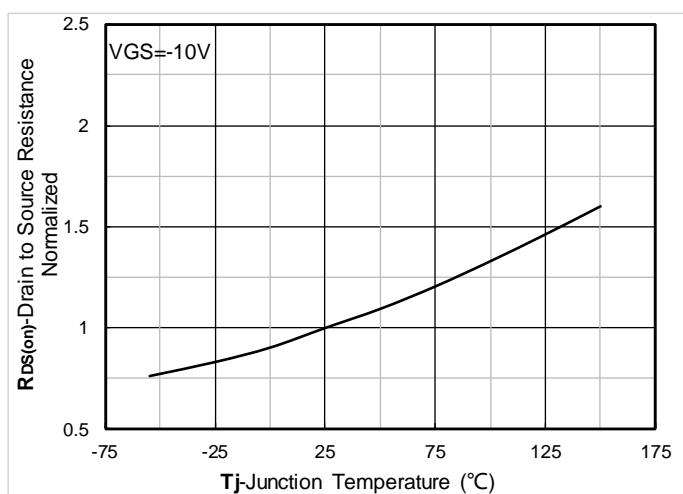


Figure 6. Normalized On-Resistance

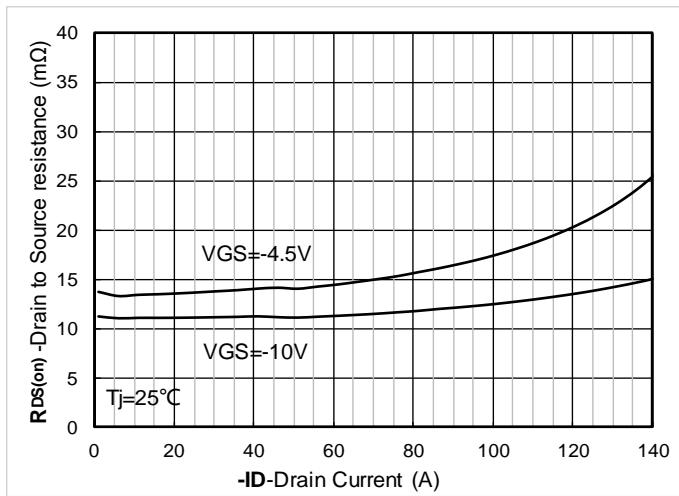
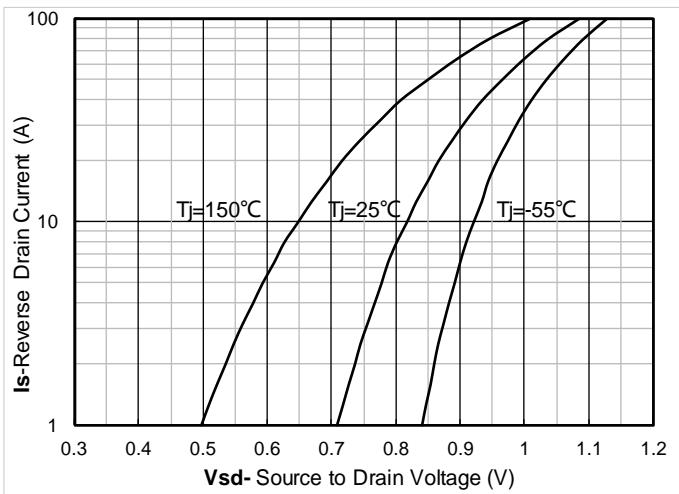
Figure 7. $R_{DS(on)}$ VS Drain Current

Figure 8. Forward characteristics of reverse diode

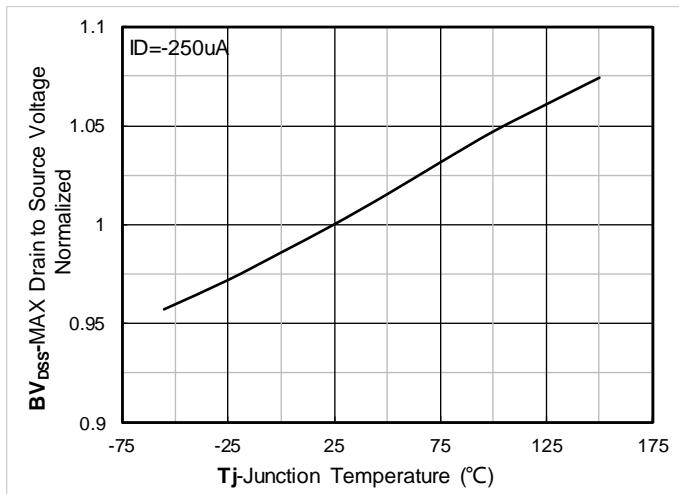


Figure 9. Normalized breakdown voltage

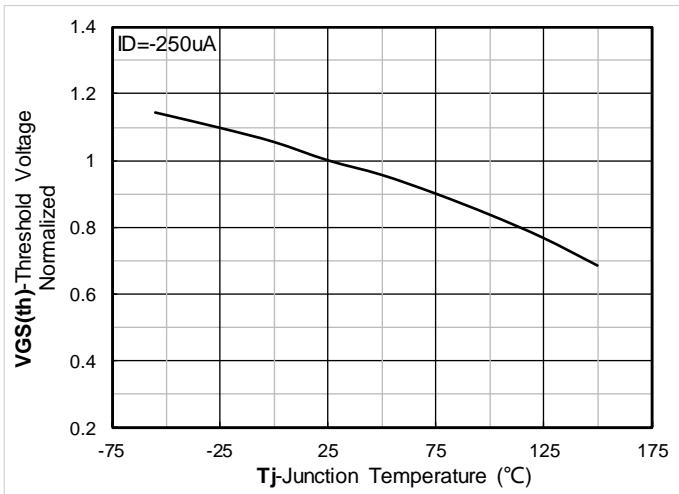


Figure 10. Normalized Threshold voltage

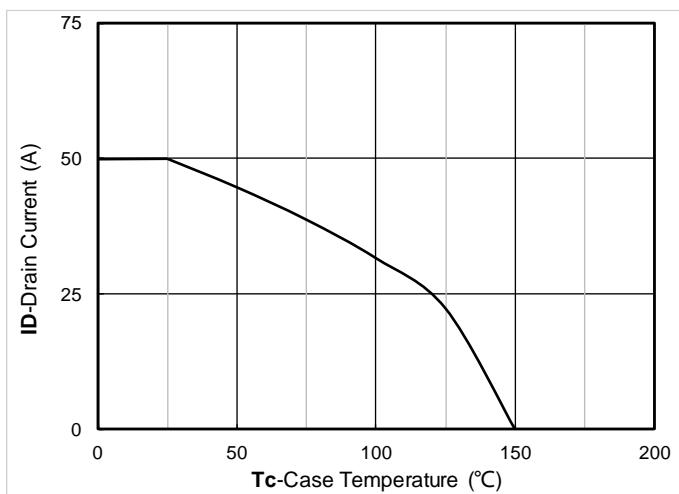


Figure 11. Current dissipation

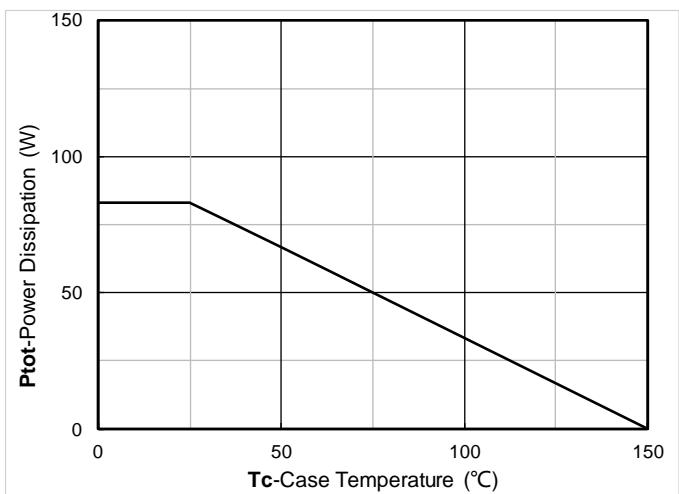


Figure 12. Power dissipation



YJD50P04AJ

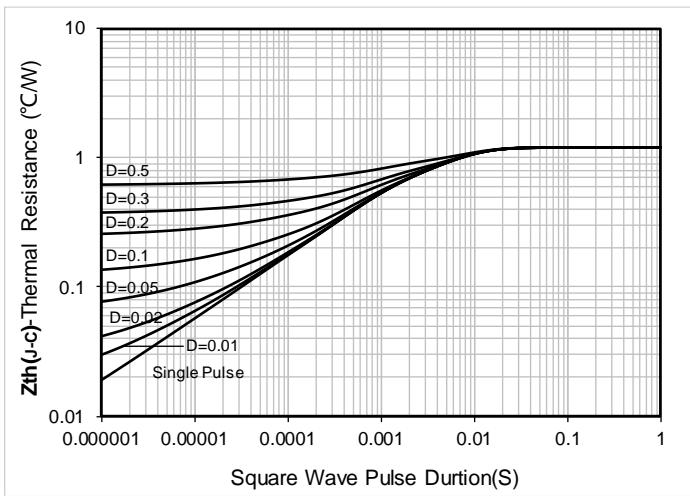


Figure 13. Maximum Transient Thermal Impedance

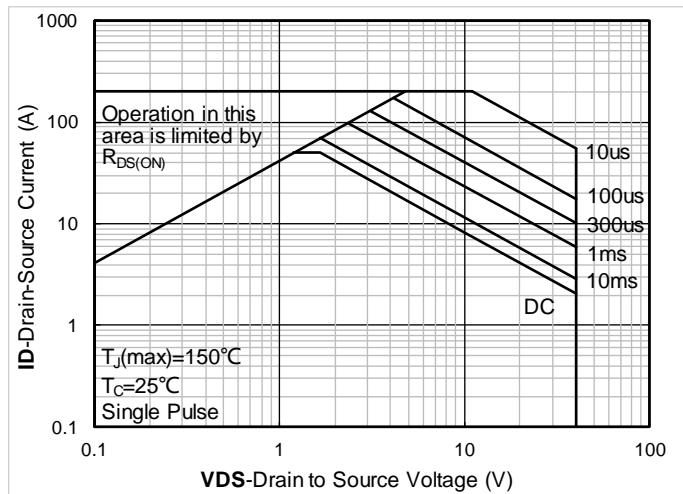
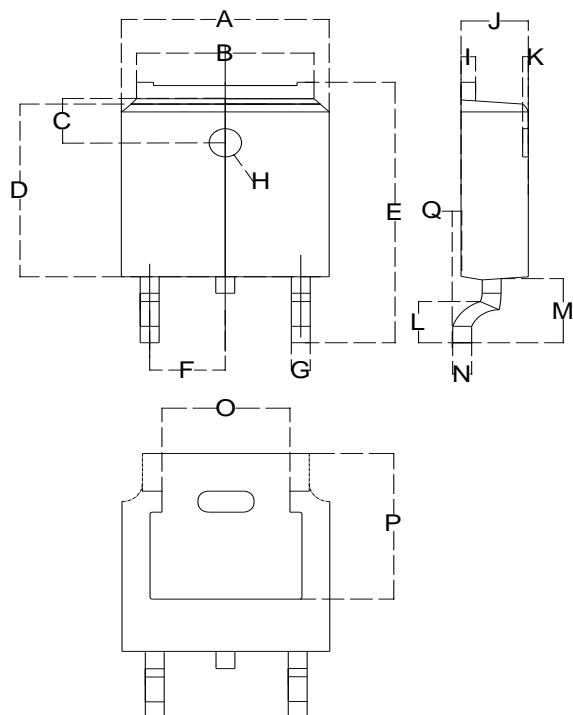


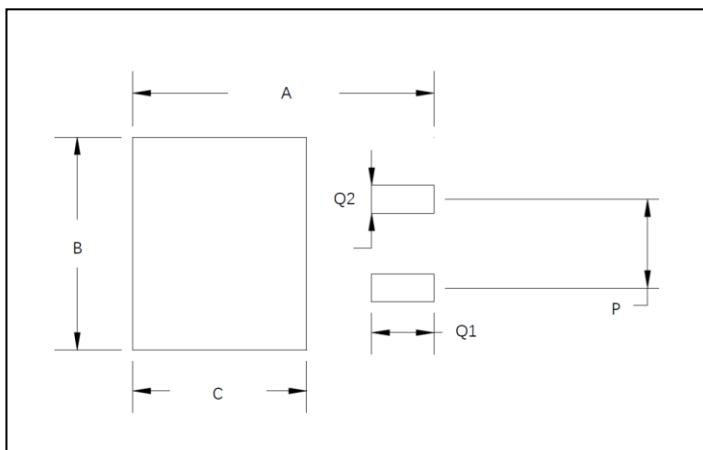
Figure 14. Safe Operation Area

**■ TO-252 Package information**

TO-252

**Dimensions in millimeters**

TO-252		
Dim	Min	Max
A	6.500	6.700
B	5.100	5.460
C	1.400	1.800
D	6.000	6.200
E	10.000	10.400
F	2.166	2.366
G	0.660	0.860
H	Φ 1.050	Φ 1.350
I	0.460	0.580
J	2.200	2.400
K	0	0.300
L	0.890	2.290
M	2.730	3.080
N	0.430	0.580
O	4.20	4.95
P	5.15	5.45
Q	0	0.2

■ Suggested Pad Layout

Dim	Millimeters
A	11.4
B	6.74
C	6.23
P	4.56
Q1	2.28
Q2	1.52



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